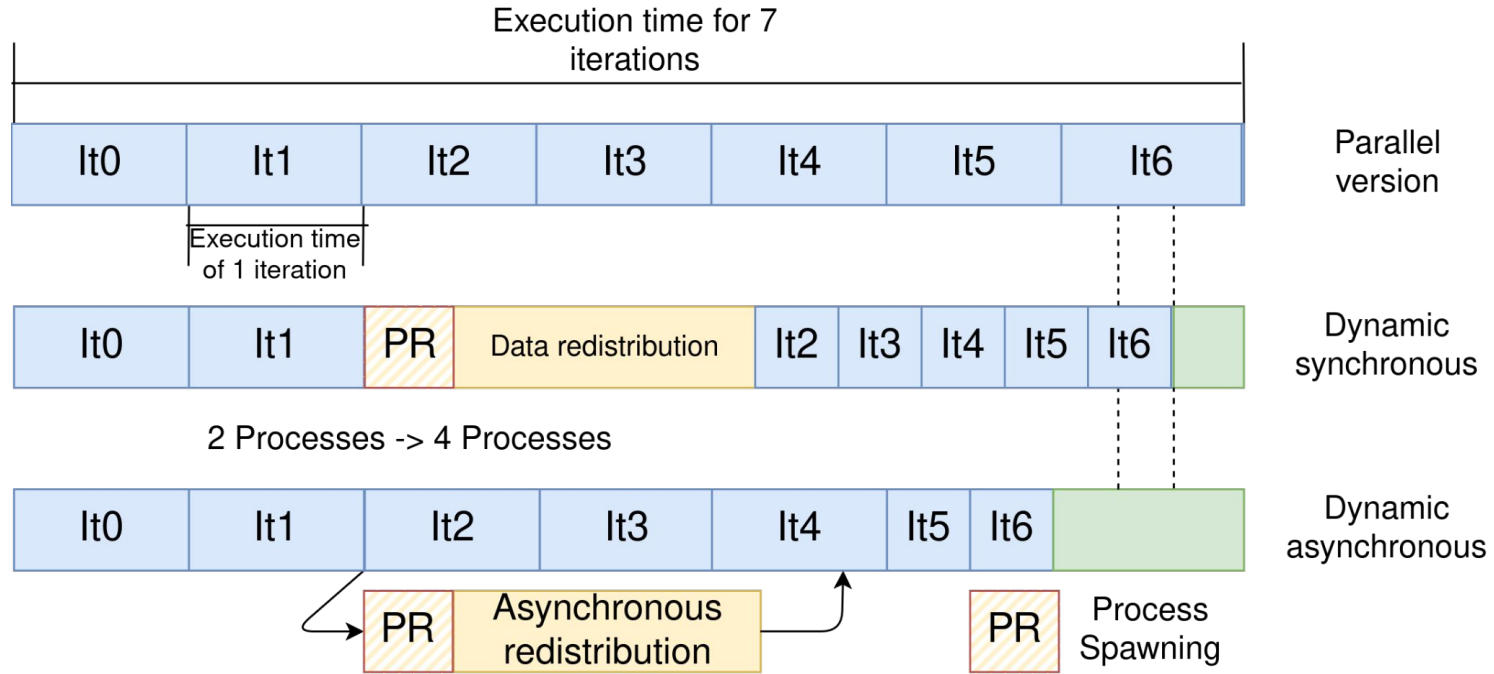


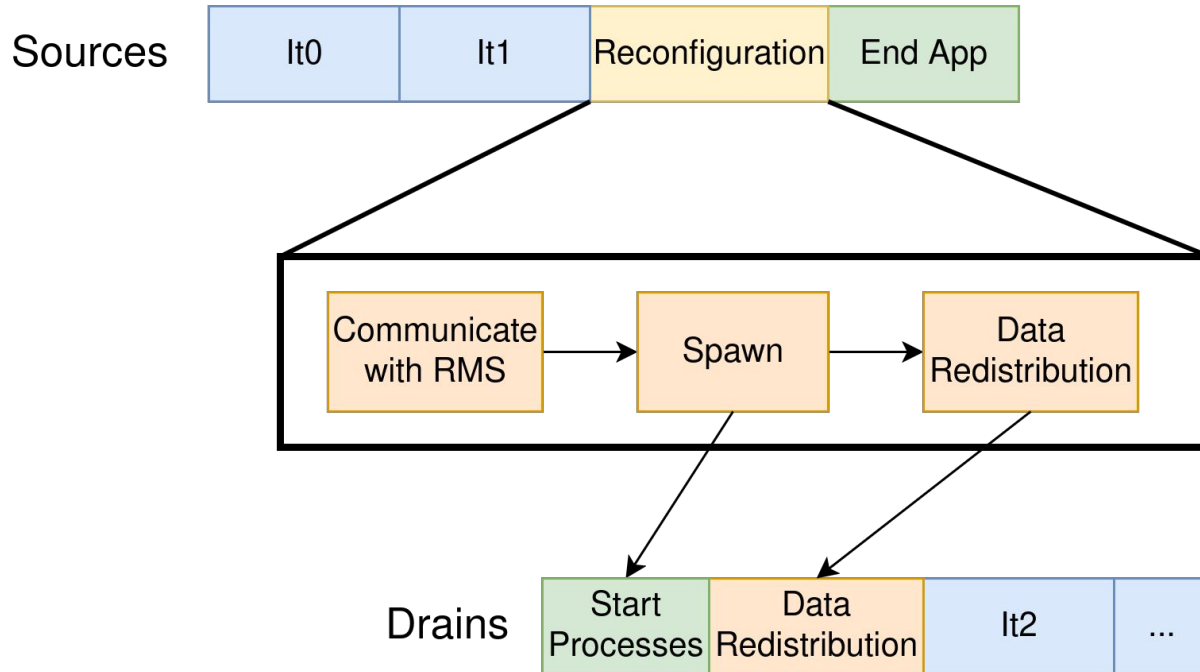


Dynamic reconfiguration for malleable applications using RMA

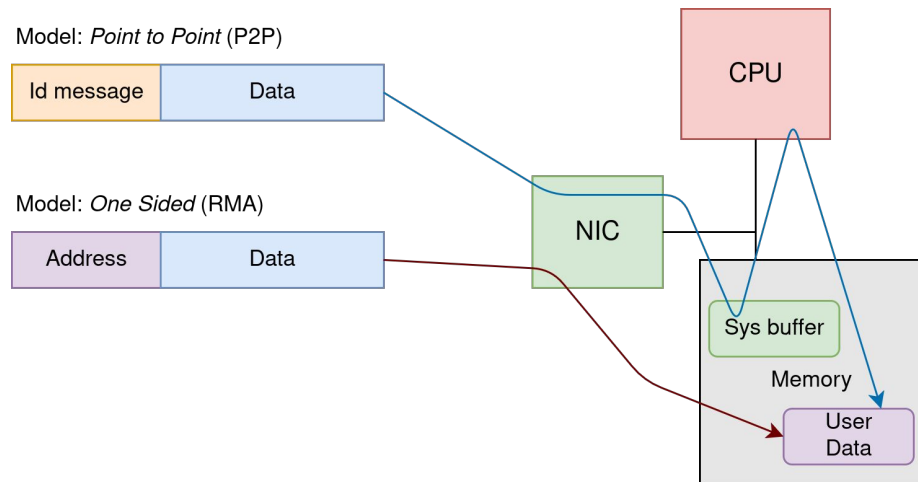
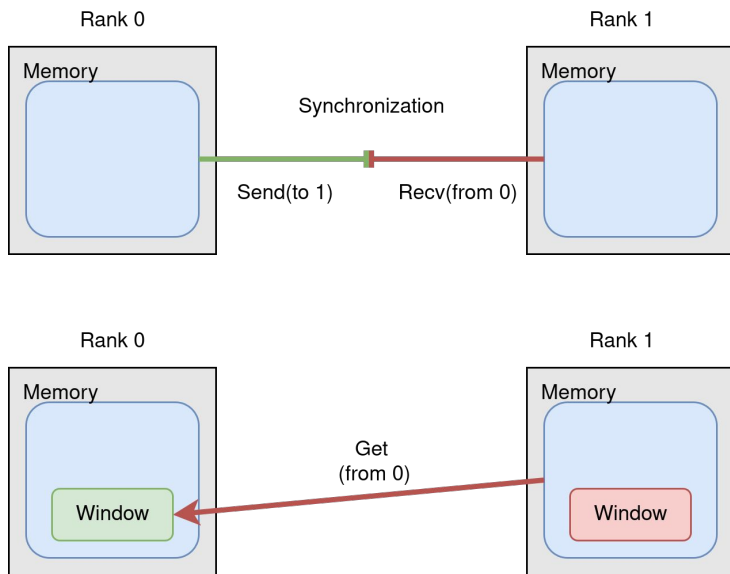
Authors

Iker Martín-Álvarez, José I. Aliaga, Maribel Castillo,





MPI One-Sided (RMA) benefits



Index:

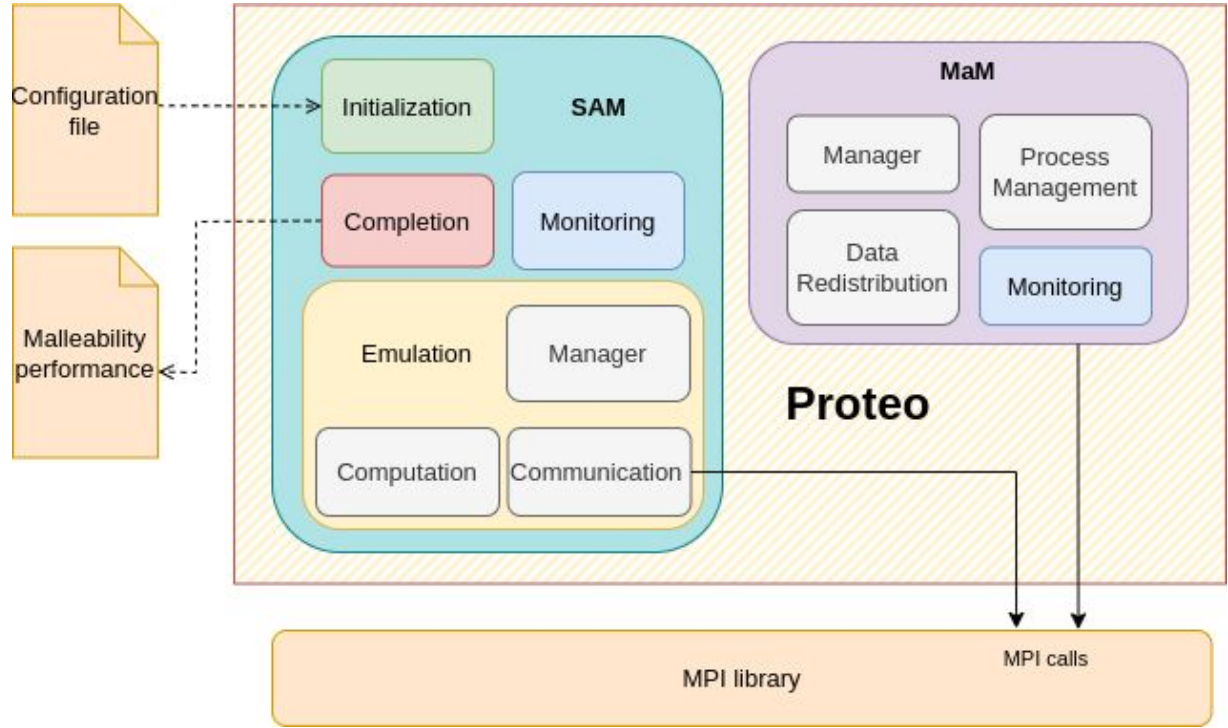
1. Objective
2. Proteo
 - 3.1 Summary
 - 3.2 Methods to spawn processes
 - 3.3 Methods to redistribute data
3. RMA implementation
4. Asynchronous implementation
5. Results
6. Conclusions

Summary of Proteo

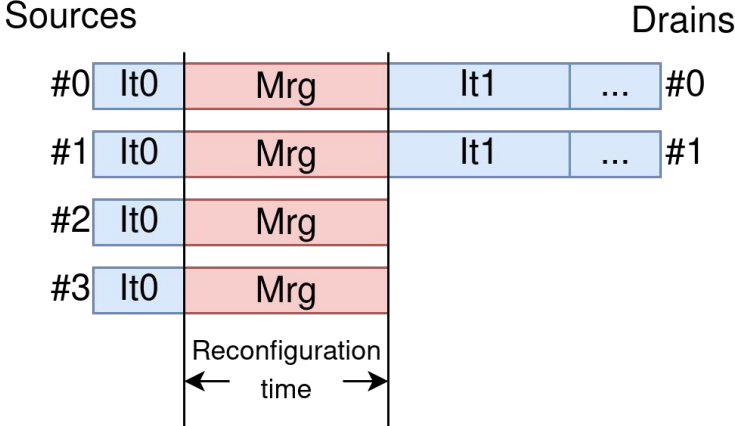
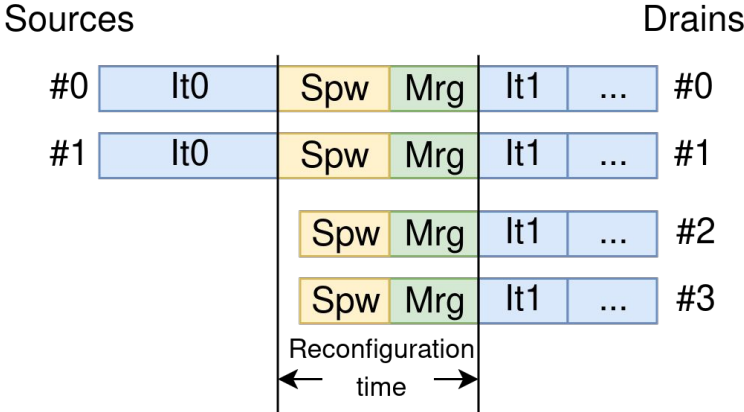
- Framework to emulate real applications.
- Composed by two modules:
 - SAM: Performs the emulation.
 - MaM: Takes cares of reconfigurations.
- Requires a configuration file to emulate the real application.



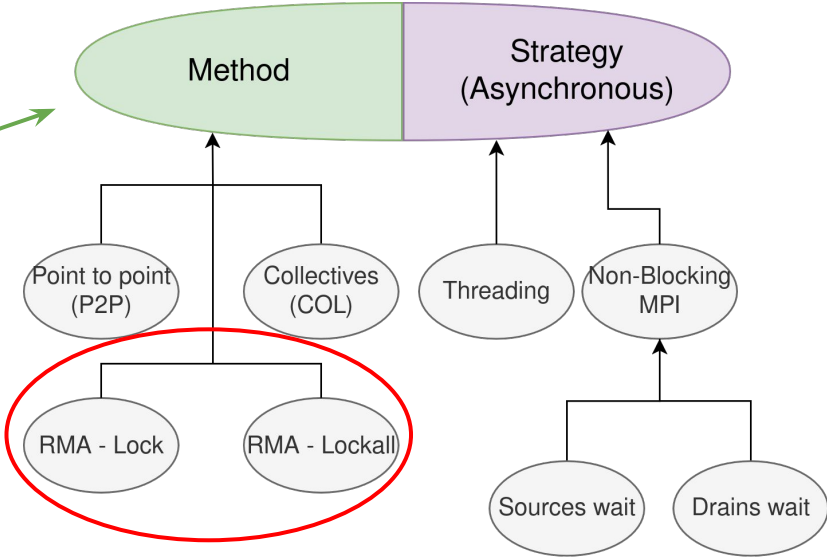
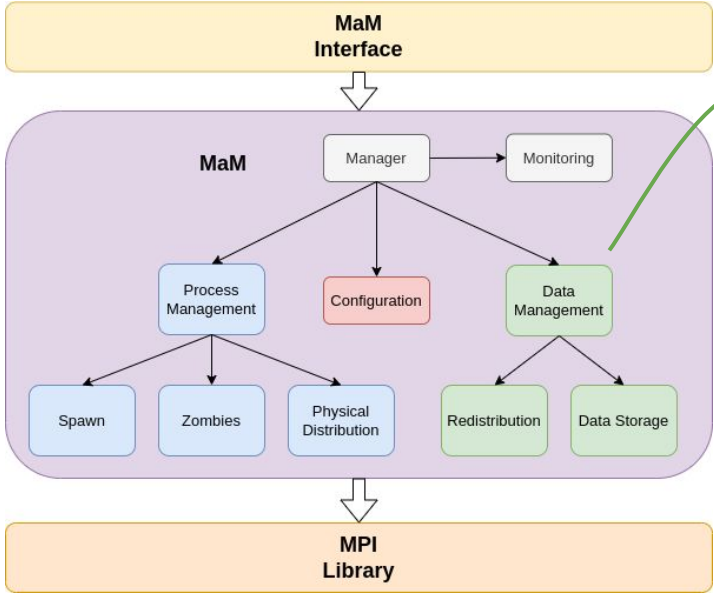
[Proteo](#)
[Repository](#)



Spawn Methods - Merge



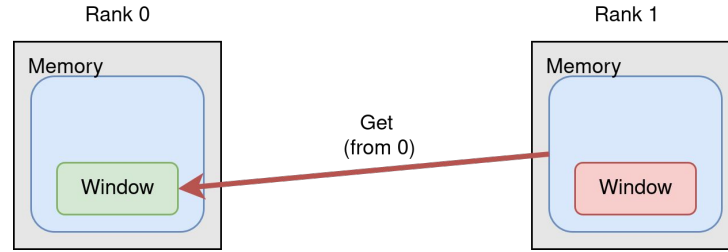
Data redistribution methods



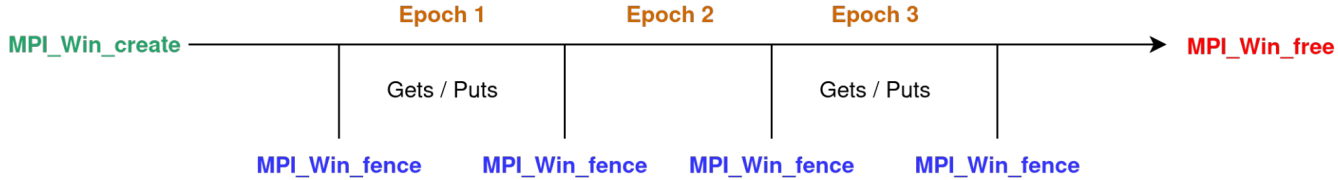
Index:

1. Objective
2. Proteo
3. RMA implementation
4. Asynchronous Implementation
5. Results
6. Conclusions

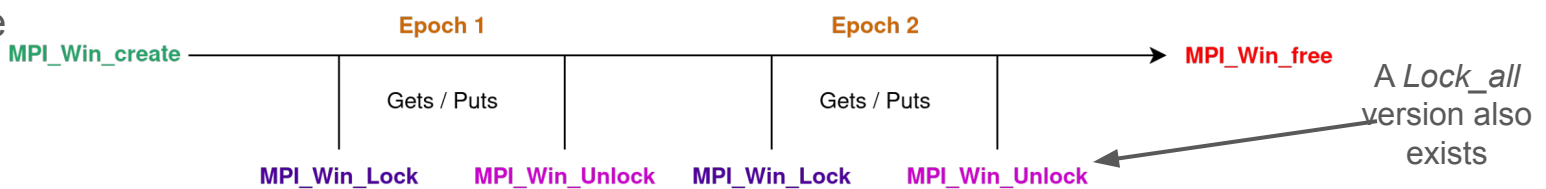
Basics



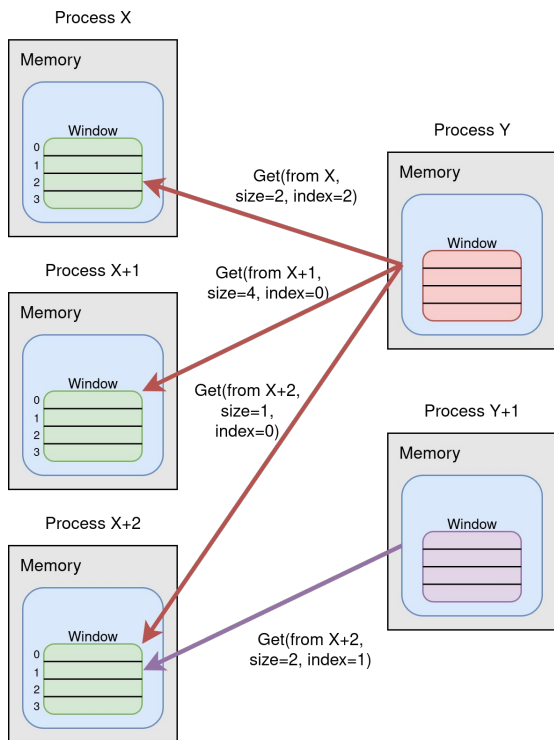
Active model



Passive model

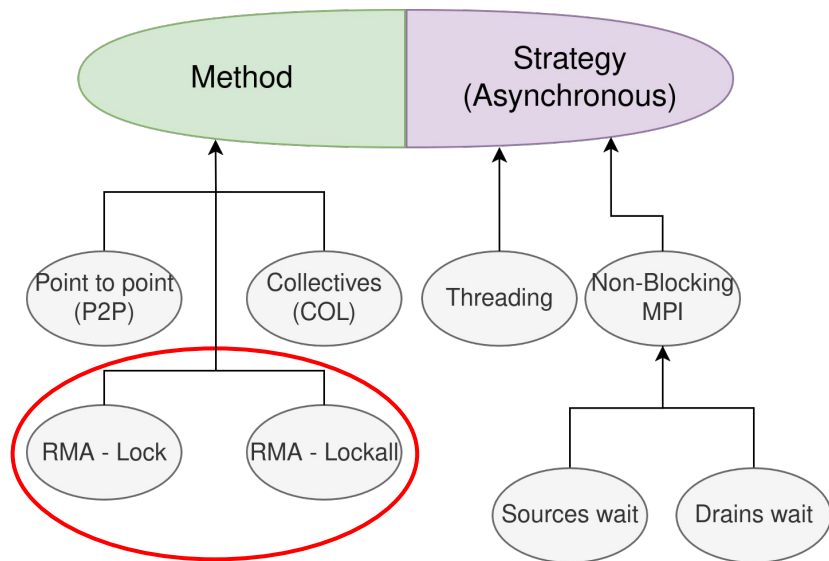


Proposed methods

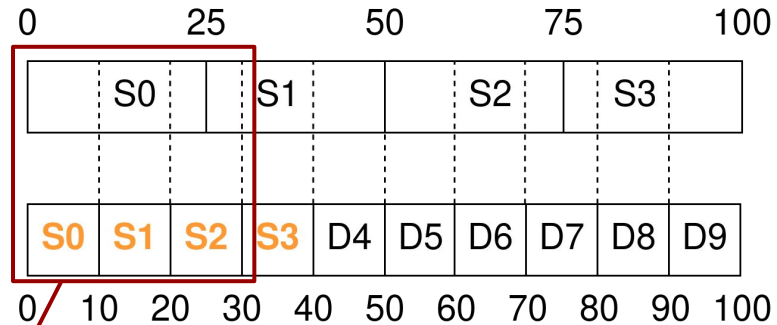
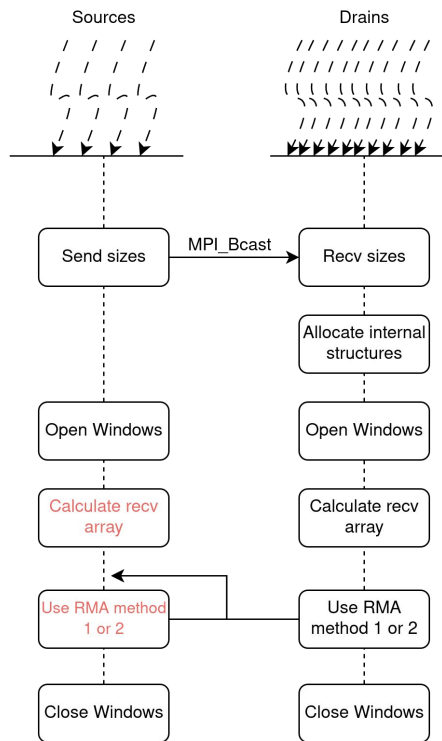


- First method:
1. Loop Lock(x) + Get(x)
 2. Loop Unlocks

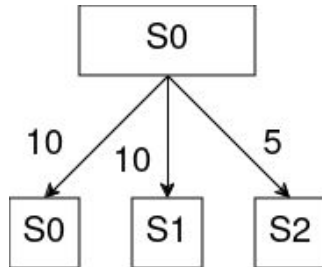
- Second method:
1. Lock_all
 2. Loop Gets
 3. Unlock_all



Merge - Steps



Distribution for 100 elements between 4 Sources and 10 Drains.



Elements distribution from Source 0 to Drains 0, 1 and 2.

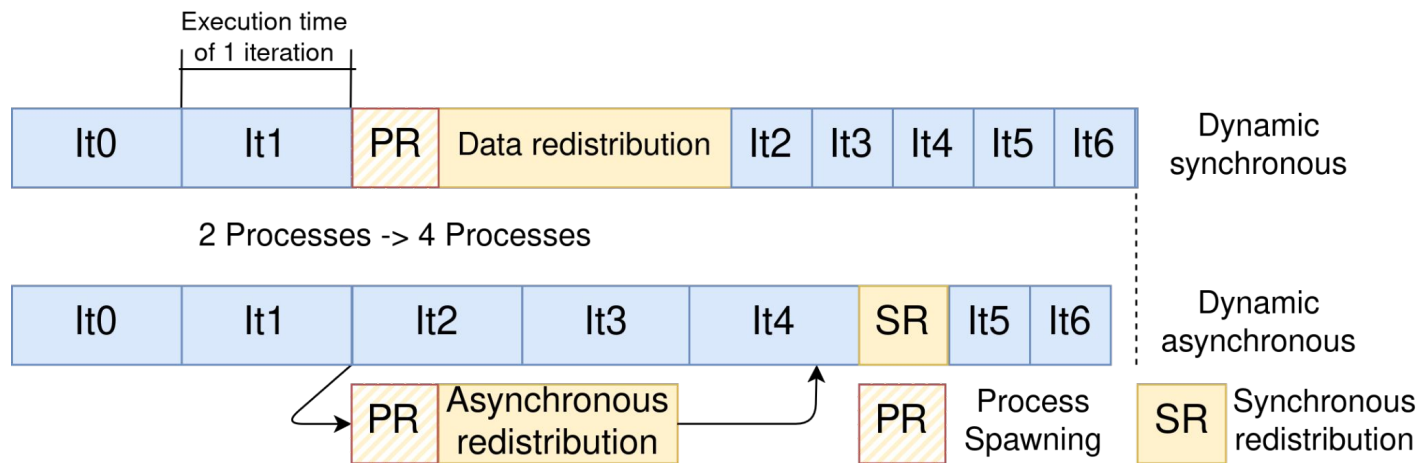
Index:

1. Objective
2. Proteo
3. RMA implementation
4. Asynchronous implementation
 - 5.1 Asynchronous benefits
 - 5.2 Strategies
 - 5.3 Operating flowchart
5. Results
6. Conclusions

Asynchronous benefits

Three strategies to redistribute data:

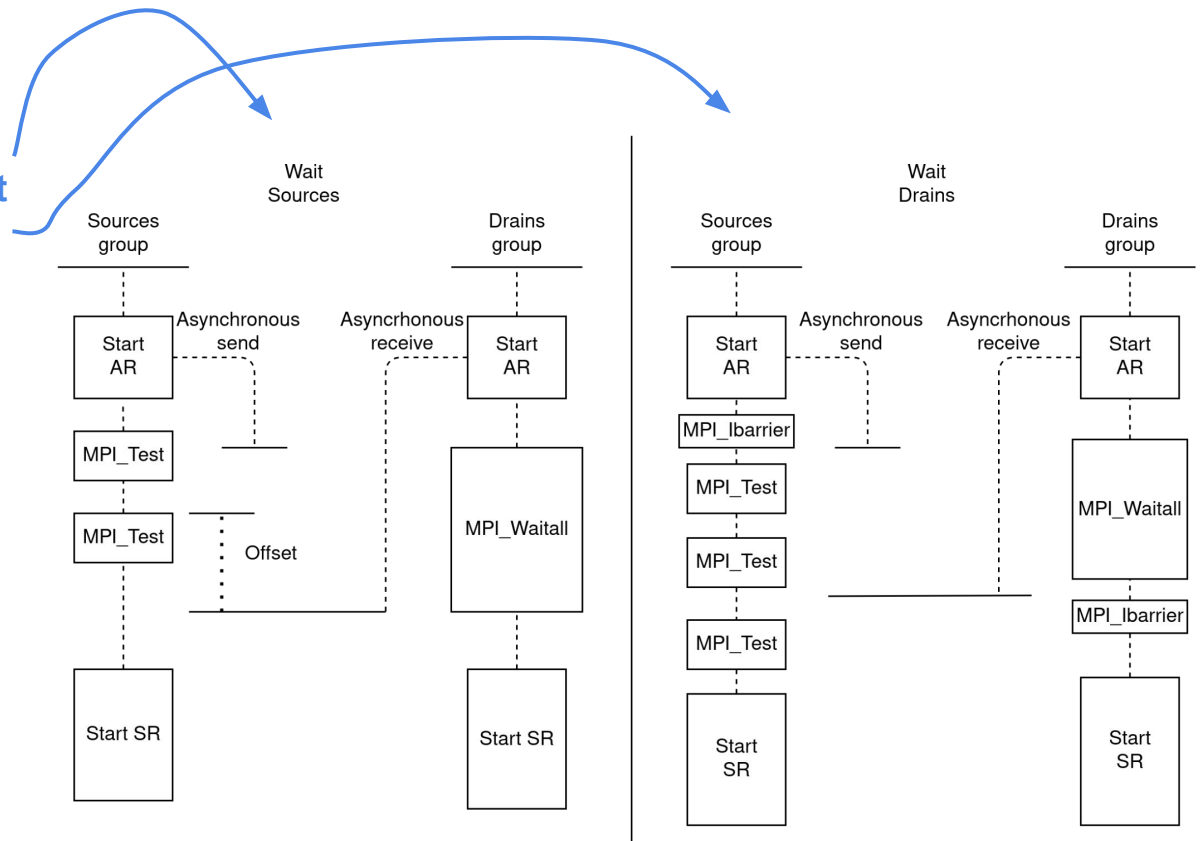
- Non-Blocking
 - Sources Wait
 - Drains Wait
- Threading



Asynchronous strategies

Three strategies to redistribute data:

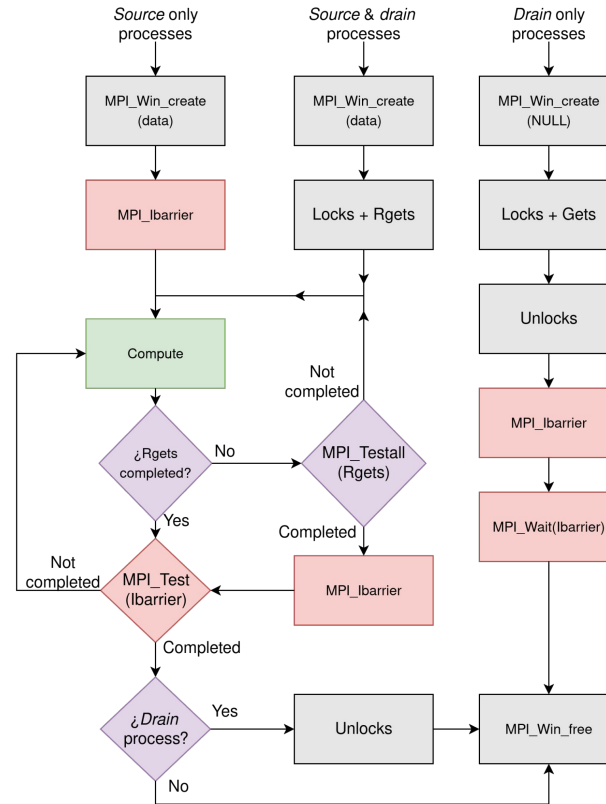
- **Non-Blocking - Sources Wait**
- **Non-Blocking - Drains Wait**
- Threading



Operating flowchart

Steps for non-blocking model:

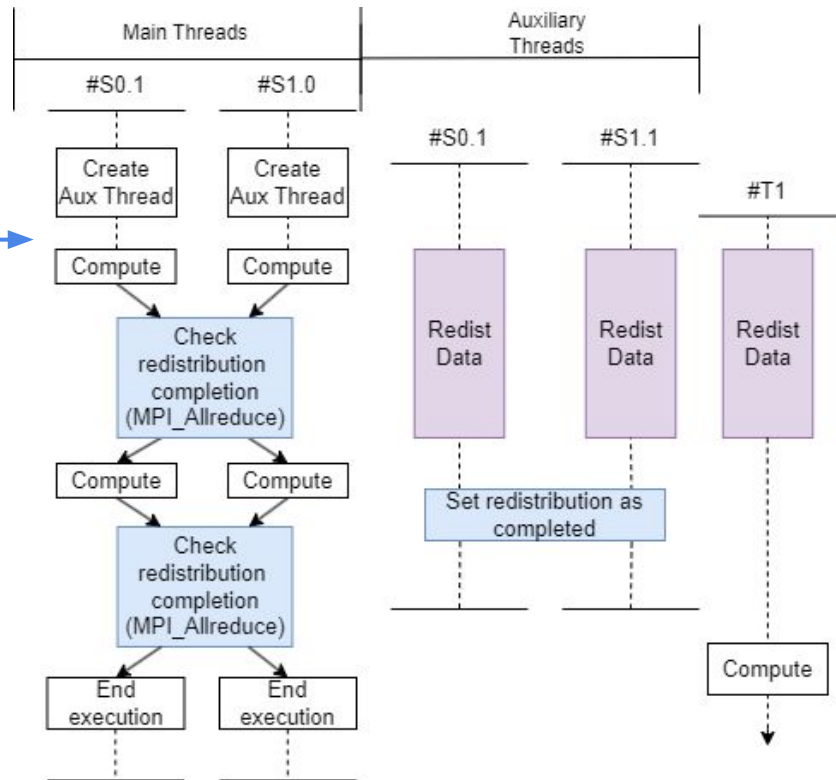
- Initialization
- Start readings
- Check local readings
- Check global readings
- Free windows



Asynchronous strategies

Three strategies to redistribute data:

- Non-Blocking - Sources Wait
- Non-Blocking - Drains Wait
- **Threading**



Index:

1. Objective
2. Proteo
3. RMA implementation
4. Asynchronous implementation
5. Results
 - 6.1 Testbed
 - 6.2 Synchronous times
 - 6.3 Asynchronous times
6. Conclusions

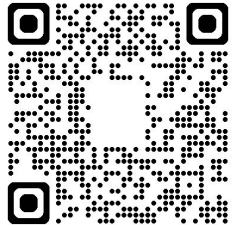
Testbed

Machine description:

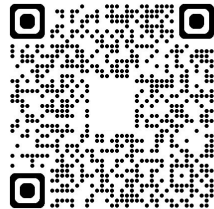
- Compute nodes (x8): 2x Intel Xeon 4210 (10-core)
- Total of 160 cores
- Network *Infiniband EDR 100GB/s*
- MPICH 4.2.0 CH4:OFI - fi_provider=verbs

Emulation description:

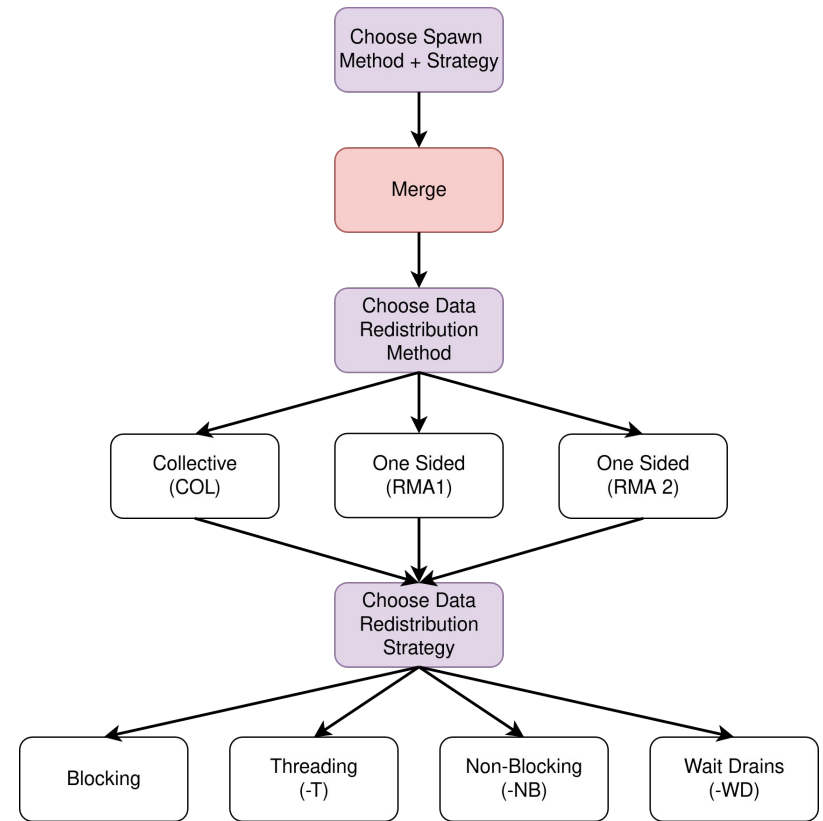
- Proteo emulates the Conjugate Gradient.
- One reconfiguration per execution.
- Data redistribution of 64GB.
- Median of 20 executions.



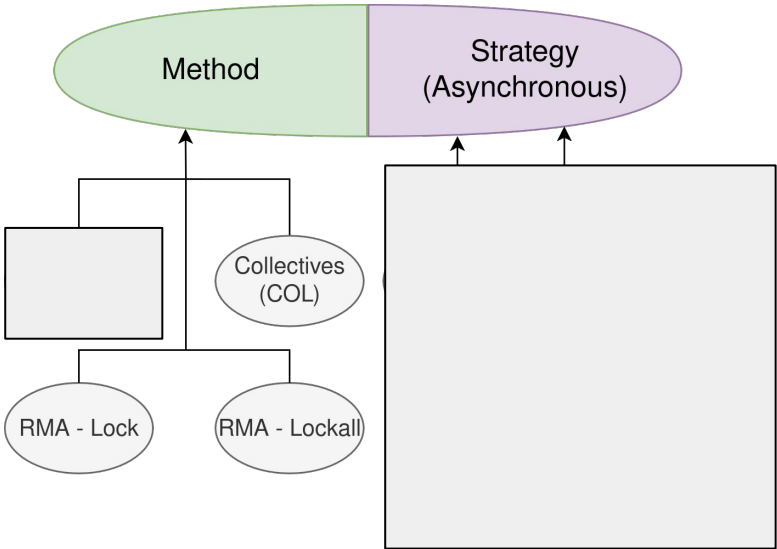
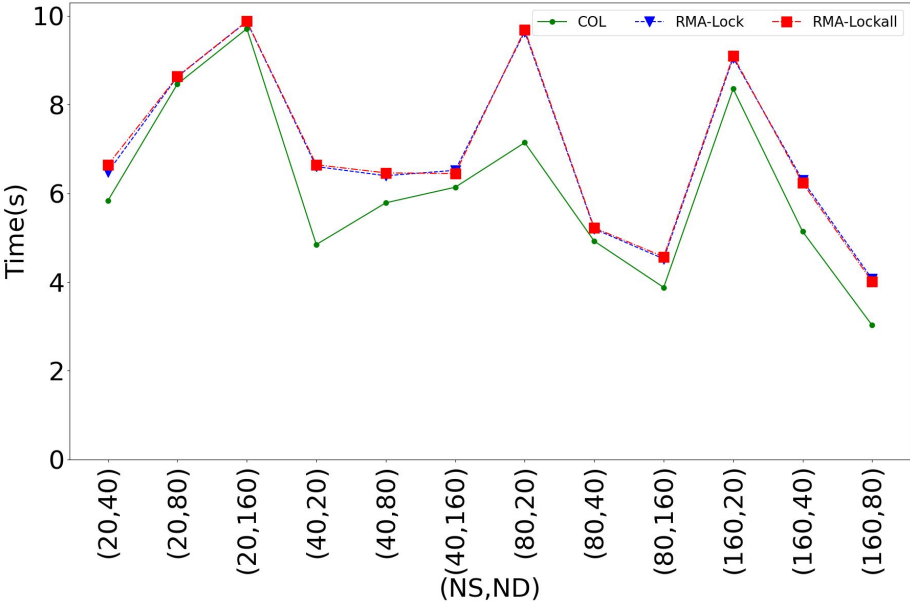
[Generated Dataset](#)



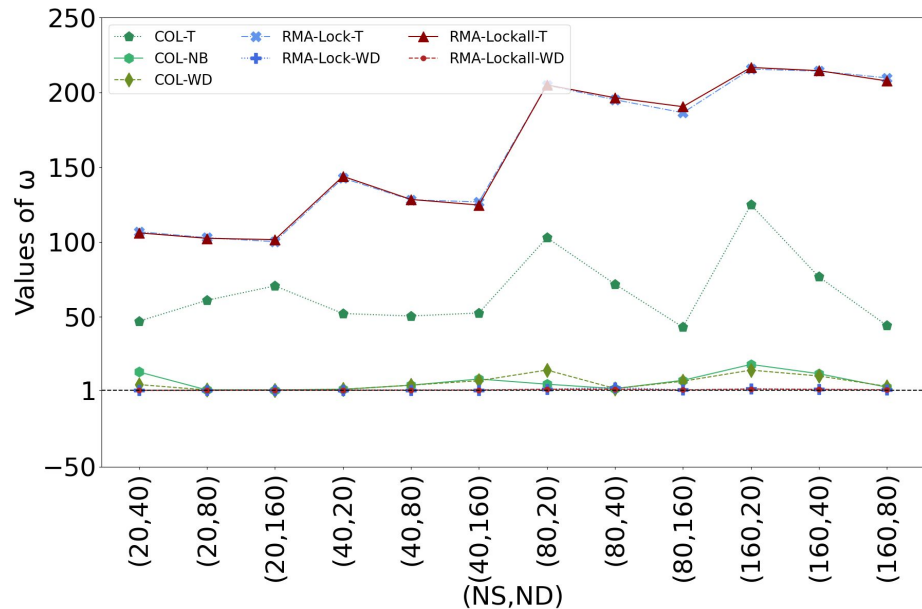
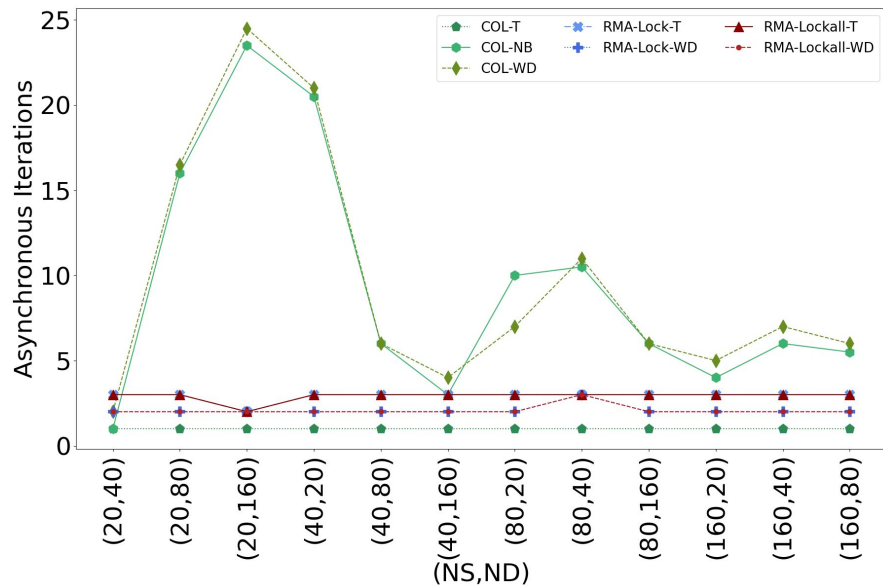
[Proteo Repository](#)



Synchronous times - Data redistribution

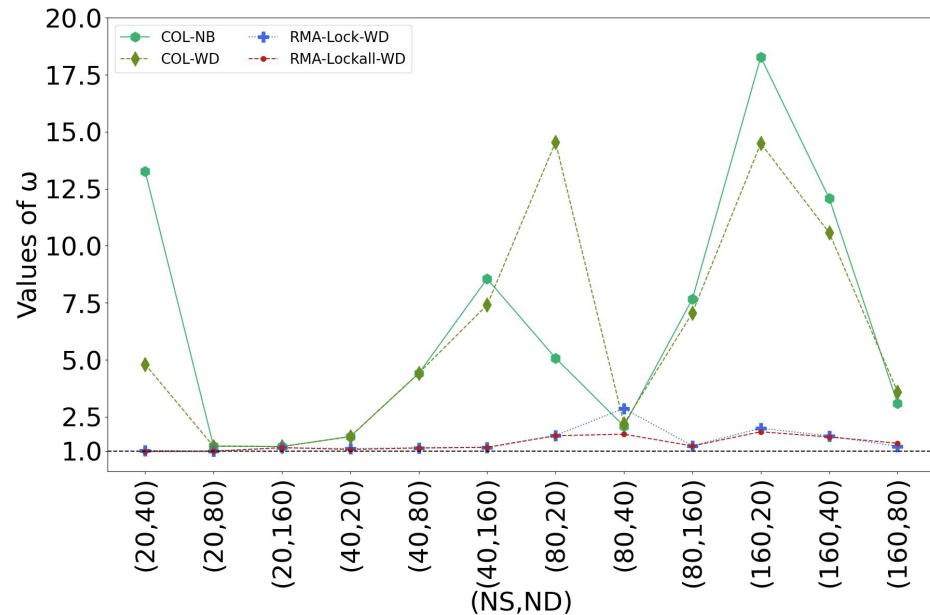
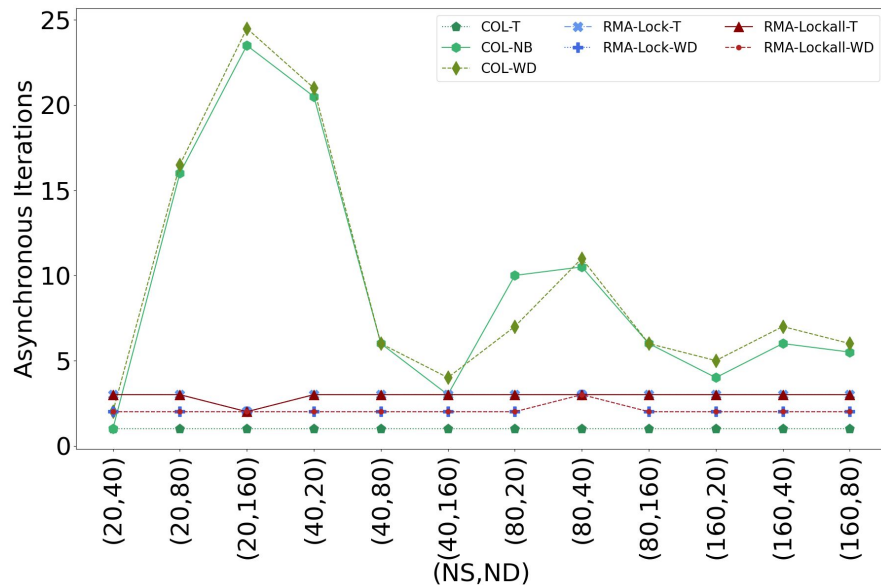


Asynchronous times - Internal parameters



ω : Iteration overcost while performing a background reconfiguration

Asynchronous times - Internal parameters



ω : Iteration overcost while performing a background reconfiguration

Asynchronous times - Data redistribution

$$T_{total}^{Bl} = T_{redis}^{Bl} + T_{it}^{NT} * \min_{variantes} (N_{it}^{NS \rightarrow NT})$$

$$T_{total}^{SP} = T_{redis}^{SP}$$

PR Process Spawning

PR Data redistribution It2 It3 Remainder app...

Execution time of 1 iteration

PR It2 It3 Remainder app...

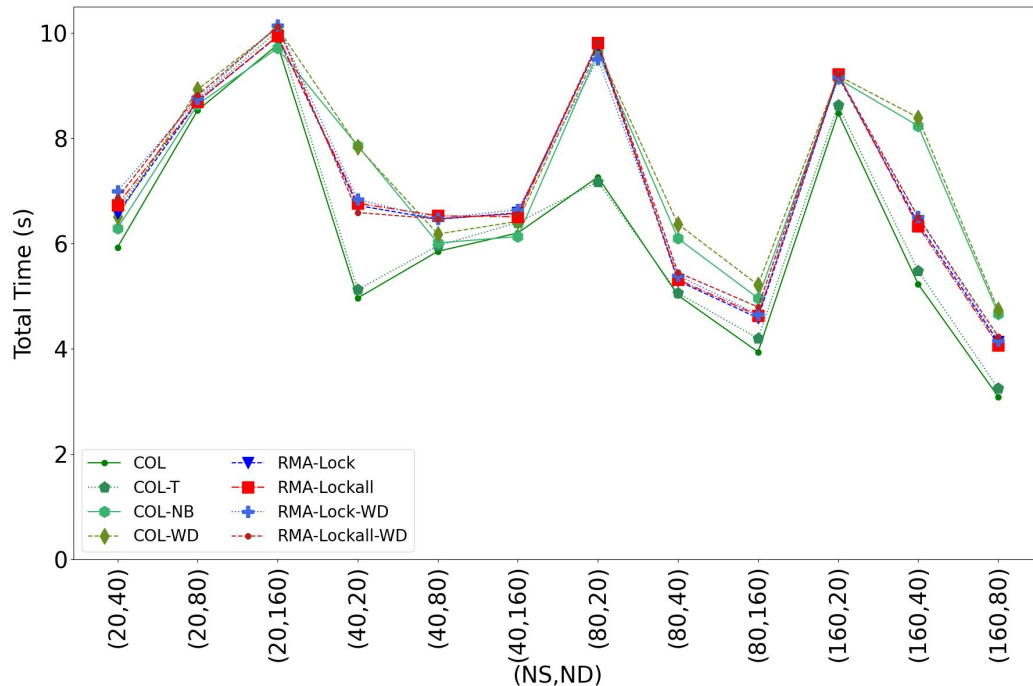
Asynchronous redistribution

PR It2 It3 It4 Remainder app...

Asynchronous redistribution

PR It2 It3 It4 Remainder app...

Asynchronous redistribution



Index:

1. Objective
2. Proteo
3. RMA implementation
4. Asynchronous implementation
5. Results
6. Conclusions

MaM has been extended with One-Sided communications for data redistribution

The asynchronous One-Sided model for data redistribution does not impact the performance of the application

Yet, the proposed model does not outstand the collective model because the initialization of the One-Sided windows



Dynamic reconfiguration for malleable applications using RMA

Authors

Iker Martín-Álvarez (martini@uji.es),

José I. Aliaga, Maribel Castillo,