QR Factorization algorithms on SMP & multi-core

PDP'08

Motivation

Basic QR

Parallelizatio

Algorithm-by-

Result

Remark

# Scheduling of QR Factorization Algorithms on SMP and Multi-core Architectures

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16th EuroMicro PDP, 2008



### Motivation

QR Factorization algorithms on SMP & multi-core

#### Motivation

Dabio **Q**...

**Parallelizatio** 

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New dense linear algebra libraries for multicore processors

- Scalability for manycore
- Data locality
- Heterogeneity?

### Motivation

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#### Motivation

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#### LAPACK (Linear Algebra Package)

- Fortran-77 codes
- One routine (algorithm) per operation in the library
- Storage in column major order

- Parallelism extracted from calls to multithreaded BLAS
- Extracting parallelism only from BLAS limits the scalability of the solution!
- Column major order does hurt data locality



### Motivation

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#### FLAME (Formal Linear Algebra Methods Environment)

- Libraries of algorithms, not codes
- Notation reflects the algorithm
- APIs to transform algorithms into codes
- Systematic derivation procedure (automated using MATHEMATICA)
- Storage and algorithm are independent

- Parallelism dictated by data dependencies, extracted at execution time
- Storage-by-blocks



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Overview of FLAME

Opening Practical QR

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New algorithm-by-blocks

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# The QR Factorization

QR Factorization algorithms on SMP & multi-core

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#### Definition

Given  $A \rightarrow m \times n$ ,  $m \ge n$ ,

$$A = QR$$

with  $Q \rightarrow m \times m$  orthogonal,  $R \rightarrow m \times n$  upper triangular.

#### Interest

- Solution of linear systems Ax = b
- Solution of linear-least squares problems min ||Ax b||

#### Computation via Householder reflectors

Given  $x \not\equiv 0$ , there exist a Householder reflector, defined by  $[u,\eta,\beta]:=h(x)$ , such that all entries of h(x)x except the first one equal zero

## The QR Factorization: Whiteboard Presentation

QR Factorization algorithms on SMP & multi-core

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Motivation

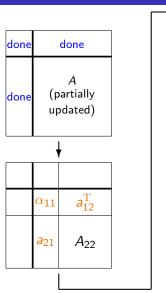
Basic QR

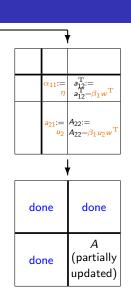
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### **FLAME Notation**

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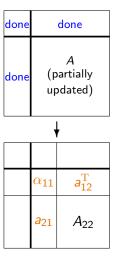
Basic QR

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#### Repartition

$$\begin{pmatrix} A_{TL} & A_{TR} \\ \hline A_{BL} & A_{BR} \end{pmatrix}$$

$$\rightarrow \begin{pmatrix} A_{00} & a_{01} & A_{02} \\ \hline a_{10}^{\mathrm{T}} & \alpha_{11} & a_{12}^{\mathrm{T}} \\ \hline A_{20} & a_{21} & A_{22} \end{pmatrix}$$

where  $\alpha_{11}$  is a scalar

### **FLAME Notation**

QR Factorization algorithms on SMP & multi-core

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**Algorithm:** 
$$[A, b] := QR_{UNB}(A)$$

Partition 
$$A \rightarrow \begin{pmatrix} A_{TL} & A_{TR} \\ A_{BL} & A_{BR} \end{pmatrix}, b \rightarrow \begin{pmatrix} b_T \\ b_B \end{pmatrix}$$

where  $A_{TL}$  is  $0 \times 0$ ,  $b_T$  has 0 elements while  $n(A_{BR}) \neq 0$  do

#### Repartition

$$\left(\begin{array}{c|c|c} A_{TL} & A_{TR} \\ \hline A_{BL} & A_{BR} \end{array}\right), \to \left(\begin{array}{c|c|c} A_{00} & a_{01} & A_{02} \\ \hline a_{10}^T & \alpha_{11} & a_{12}^T \\ \hline A_{20} & a_{21} & A_{22} \end{array}\right), \ b \to \left(\begin{array}{c|c} b_0 \\ \hline \beta_1 \\ \hline b_2 \end{array}\right)$$

where  $\alpha_{11}$  and  $\beta_1$  are scalars

$$\begin{aligned} & [u_2, \eta, \beta_1] := h\left(\alpha_{11}, a_{21}\right) \\ & w^{\mathrm{T}} := a_{12}^{\mathrm{T}} + u_2^{\mathrm{T}} A_{22} \\ & \left(\frac{\alpha_{11} \mid a_{12}^{\mathrm{T}}}{a_{21} \mid A_{22}}\right) := \left(\frac{\eta \mid a_{12}^{\mathrm{T}} - \beta_1 w^{\mathrm{T}}}{u_2 \mid A_{22} - \beta_1 u_2 w^{\mathrm{T}}}\right) \end{aligned}$$

#### Continue with

$$\left( \begin{array}{c|c|c|c} A_{TL} & A_{TR} \\ \hline A_{BL} & A_{BR} \end{array} \right) \leftarrow \left( \begin{array}{c|c|c} A_{00} & a_{01} & A_{02} \\ \hline a_{10}^T & \alpha_{11} & a_{12}^T \\ \hline A_{20} & a_{21} & A_{22} \end{array} \right), \ b \leftarrow \left( \begin{array}{c|c} b_0 \\ \hline \beta_1 \\ \hline b_2 \end{array} \right)$$
 endwhile



#### FLAME Code

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#### From algorithm to code...

#### FI AME notation

#### Repartition

$$\left(\begin{array}{c|c|c} A_{TL} & A_{TR} \\ \hline A_{BL} & A_{BR} \end{array}\right) \to \left(\begin{array}{c|c|c} A_{00} & a_{01} & A_{02} \\ \hline a_{10}^{\rm T} & \alpha_{11} & a_{12}^{\rm T} \\ \hline A_{20} & a_{21} & A_{22} \end{array}\right)$$

where  $\alpha_{11}$  is a scalar

#### FLAME/C code

#### FLAME Code

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```
int FLA_QR_unb( FLA_Obj A, FLA_Obj b )
 /* ... FLA Part 2x2(): ... */
 while (FLA_Obj_width(ABR) != 0){
  &a10t, /**/ &alpha11, &a12t,
                  ABL, /**/ ABR,
                              &A20, /**/ &a21, &A22,
                 1, 1, FLA_BR );
  /* ... */
  /*----*/
  FLA_House( eta, alpha11, a21 ); /* [ u_2, eta, beta_1 ] :=
                            h(alpha_11, a21) */
                         /* alpha 11 := beta 1 */
                         /* a21 := u2
  FLA_Copy( a12t, wt );
                         /* wt := -(a12t
                                  +u2^t * A22)*/
  FLA Gemv (FLA TRANSPOSE, FLA ONE,
         A22, u2, FLA_ONE, wt );
  FLA_Axpy( beta1, wt, a12t ); /* a_12t :=
                            a 12t + beta 1 * wt */
  FLA_Ger ( beta1, u2, wt, A22 ); /* A22 :=
                           A22 + beta 1 * u2 * wt */
  /*-----*/
  /* FLA_Cont_with_3x3_to_2x2( ); ... */
```

### FLAME Code

QR Factorization algorithms on SMP & multi-core

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Motivation

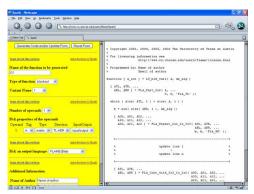
Basic QR

Algorithm-by-

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Visit http://www.cs.utexas.edu/users/flame/Spark/...



- M-script code for MATLAB: FLAME@lab
- C code: FLAME/C
  - Other APIs:
    - FLATEX
    - Fortran-77
    - LabView
    - Message-passing parallel: PLAPACK
    - FLAG: GPUs
    - FLAOOC: Out-of-Core

### Outline

QR Factorization algorithms on SMP & multi-core

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- Motivation
- Basic QR
- Practical QR

Blocked algorithm and use of BLAS for high-performance

- Parallelization
- New algorithm-by-blocks
- 6 Experimental results
- Concluding remarks



# Blocked Algorithm for High Performance

QR Factorization algorithms on SMP & multi-core

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Remark

**Algorithm:** 
$$[A, S] := QR_BLK(A)$$

Partition ...

where ...

while  $n(A_{BR}) \neq 0$  do

Determine block size b

Repartition

$$\left(\begin{array}{c|c}
A_{TL} & A_{TR} \\
\hline
A_{BL} & A_{BR}
\end{array}\right) \rightarrow \left(\begin{array}{c|c}
A_{00} & A_{01} & A_{02} \\
\hline
A_{10} & A_{11} & A_{12} \\
\hline
A_{20} & A_{21} & A_{22}
\end{array}\right), \left(\begin{array}{c}
S_T \\
\hline
S_B
\end{array}\right) \rightarrow \left(\begin{array}{c}
S_0 \\
\hline
S_1 \\
\hline
S_2
\end{array}\right)$$

where  $A_{11}$  is  $b \times b$  ,  $S_1$  has b rows

$$\overline{\left[\left(\frac{A_{11}}{A_{21}}\right), \ b_1\right]} = \overline{\left[\left(\frac{\{U\backslash R\}_{11}}{U_{21}}\right), \ b_1\right]} := \operatorname{QR}_{\operatorname{UNB}}\left(\left(\frac{A_{11}}{A_{21}}\right)\right)$$
Compute  $S_1$  from  $A_{11}$ ,  $A_{12}$ ,  $b_1$ 

$$W := \begin{pmatrix} U_{11}^{\mathrm{T}} \mid U_{21}^{\mathrm{T}} \end{pmatrix} \begin{pmatrix} A_{12} \\ A_{22} \end{pmatrix}$$
$$\begin{pmatrix} A_{12} \\ A_{22} \end{pmatrix} := \begin{pmatrix} A_{12} \\ A_{22} \end{pmatrix} + \begin{pmatrix} U_{11} \\ U_{21} \end{pmatrix} S_1 W$$

Continue with

endwhile



# Blocked Algorithm for High Performance

**QR Factorization** algorithms on SMP & multi-core

Basic OR

#### LAPACK implementation: kernels in BLAS

$$A_{11}$$
 is  $b \times b$ 

1. QR\_UNB 
$$\left(\frac{A_{11}}{A_{21}}\right)$$

Unblk. QR, 
$$O(nb^2)$$
 flops

2. 
$$W := \left( \begin{array}{c|c} U_{11}^{\mathrm{T}} & U_{21}^{\mathrm{T}} \end{array} \right) \left( \begin{array}{c} A_{12} \\ \hline A_{22} \end{array} \right)$$

GEMM, 
$$O(nb^2)$$
 flops

3. 
$$\left(\frac{A_{12}}{A_{22}}\right) := \left(\frac{A_{12}}{A_{22}}\right) + \left(\frac{U_{11}}{U_{21}}\right) S_1 W$$
 GEMM,  $O(n^2b)$  flops

### Outline

QR Factorization algorithms on SMP & multi-core

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- Motivation
- Basic QR
- Practical QR
- Parallelization

Control-flow vs. data-flow parallelism Storage-by-blocks API

- New algorithm-by-blocks
- Experimental results
- Concluding remarks

# Parallelization on Shared-Memory Architectures

QR
Factorization
algorithms on
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Motivation

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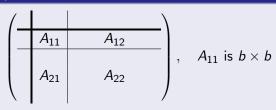
Parallelization

Algorithm-byblocks

Result

Remark

#### LAPACK parallelization: kernels in multithread BLAS



- Advantage: Use legacy code
- Drawbacks:
  - Each call to BLAS is a synchronization point for threads
  - As the number of threads increases, serial operations with cost  $O(nb^2)$  are no longer negligible compared with  $O(n^2b)$

# Parallelization on Shared-Memory Architectures

QR
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#### FLAME parallelization: SuperMatrix

- Traditional (and pipelined) parallelizations are limited by the control dependencies dictated by the code
- The parallelism should be limited only by the data dependencies between operations!
- In dense linear algebra, imitate a superscalar processor: dynamic detection of data dependencies

# FLAME Parallelization: SuperMatrix

QR Factorization algorithms on SMP & multi-core

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Remark

```
int FLA_QR_blk( FLA_Obj A, FLA_Obj S )
 /* ... FLA Part 2x2(): ... */
 while (FLA_Obj_width(ATL) < FLA_Obj_width(A)){
   /* FLA Repart 2x2 to 3x3(): ... */
   FLA QR unb( A11.
                                   /* OR( A11: A21 )
               A21, S1 ):
   FLA QR update blk( A11, A12,
                      A21, A22, S1 ):
                         := (U11^T | U_21^T ) * (A12; A22)
              (A12; A22) := (A12; A22) + (U11; U21) * S1 * W
   /* FLA_Cont_with_3x3_to_2x2( ); ... */
```

#### The FLAME runtime system "pre-executes" the code:

 Whenever a routine is encountered, a pending task is annotated in a global task queue



# FLAME Parallelization: SuperMatrix

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$$\mathbf{QR}_{\text{-UNB}}\left(\frac{A_{00}}{A_{10}}\right)$$

$$\mathbf{2} \ \left( \frac{A_{01}}{A_{11}} \right) := Q_{11}^{\mathrm{T}} \left( \frac{A_{01}}{A_{11}} \right)$$

#### SuperMatrix

 $\begin{pmatrix} A_{10} & A_{11} & A_{12} \\ A_{20} & A_{21} & A_{22} \end{pmatrix}$ 

Once all tasks are annotated, the real execution begins!

Runtime

- Tasks with all input operands available are runnable; other tasks must wait in the global queue
- Upon termination of a task, the corresponding thread updates the list of pending tasks

# FLAME Storage-by-Blocks: FLASH

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- Algorithm and storage are independent
- Matrices stored by blocks are viewed as matrices of matrices
- No significative modification to the FLAME codes

### Outline

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**Parallelization** 

Algorithm-byblocks

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- Motivation
- Basic QR
- Opening Practical QR
- Parallelization
- New algorithm-by-blocks
   Expose more parallelism
- Experimental results
- Concluding remarks



# Algorithm-by-blocks for the QR factorization

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/	/		\	
		$A_{11}$	A <sub>12</sub>	
l				$,  A_{11} \text{ is } b \times b$
1		$A_{21}$	$A_{22}$	
١	\		<i> </i>	

- ullet All operations on  $A_{22}$  must wait till  $\left( \frac{A_{11}}{A_{21}} \right)$  is factorized
- Algorithms-by-blocks for the Cholesky factorization do not present this problem
- Is it possible to design an algorithm-by-blocks for the QR factorization?



# Algorithm-by-blocks for the QR factorization

QR Factorization algorithms on SMP & multi-core

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$$\begin{pmatrix} & & & & & \\ & A_{11} & A_{12} & A_{13} \\ & A_{21} & A_{22} & A_{23} \\ & A_{31} & A_{32} & A_{33} \end{pmatrix}, \quad A_{ij} \text{ is } t \times t$$

- **1** Factorize  $Q_{11}A_{11} = R_{11}$
- ② Apply factor  $Q_{11}$ :

$$Q_{11}^{\rm T}A_{12}\ \big|\ Q_{11}^{\rm T}A_{13}$$

- **4** Apply factor  $Q_{21}$ :

$$Q_{21}^{\mathrm{T}}\left(\begin{array}{c}A_{12}\\\hline A_{22}\end{array}\right) \mid Q_{21}^{\mathrm{T}}\left(\begin{array}{c}A_{13}\\\hline A_{23}\end{array}\right)$$

Repeat steps 2-4 with A<sub>31</sub>

# Algorithm-by-blocks for the QR factorization

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/	′			\			
l		$A_{11}$	A <sub>12</sub>	A <sub>13</sub>		Λ i	$s t \times t$
l		$A_{21}$	A <sub>22</sub>	$A_{23}$	,	$A_{ij}$ is	) L ^ L
/		A <sub>31</sub>	A <sub>32</sub>	$A_{33}$			

#### Different from traditional QR factorization

- To obtain high performance a blocked algorithm with block size  $b \ll t$ , is used in the factorization and application of factors
- To maintain the computational cost, the upper triangular structure of  $A_{11}$  is exploited during the factorization



### Outline

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# **Experimental Results**

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#### General

	Platform	Specs.		
	SET	CC-NUMA with 16 Intel Itanium-2 processors		
	NEUMANN	SMP with 8 dual-core AMD Opteron processors		

#### Implementations

- LAPACK: LAPACK 3.0 routine dgetrf + multithreaded MKL
- MKL: Multithreaded routine dgetrf in MKL
- AB: Algorithm-by-blocks + serial MKL + storage-by-blocks



# **Experimental Results**

QR Factorization algorithms on SMP & multi-core

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Motivation

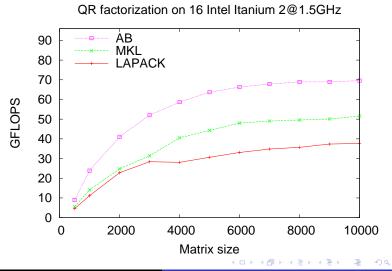
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Algorithm-byblocks

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# **Experimental Results**

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Motivation

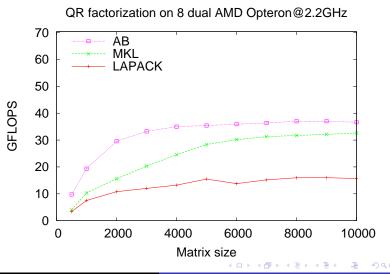
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# Concluding Remarks

QR Factorization algorithms on SMP & multi-core

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- More parallelism is needed to deal with the large number of cores of future architectures and data locality issued: traditional dense linear algebra libraries will have to be rewritten
- An algorithm-by-blocks is possible for the QR factorization similar to those of Cholesky and QR factorizations
- The FLAME infrastructure (FLAME/C API, FLASH, and SuperMatrix) reduces the time to take an algorithm from whiteboard to high-performance parallel implementation

QR Factorization algorithms on SMP & multi-core

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Thanks for your attention!

#### For more information...

Visit http://www.cs.utexas.edu/users/flame

#### Support...

- National Science Foundation awards CCF-0702714 and CCF-0540926 (ongoing till 2010).
- Spanish CICYT project TIN2005-09037-C02-02.

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#### Related publications

- E. Chan, E.S. Quintana-Ortí, G. Quintana-Ortí, R. van de Geijn.
   SuperMatrix out-of-order scheduling of matrix operations for SMP and multicore architectures. 19th ACM Symp. on Parallelism in Algorithms and Architectures SPAA'2007.
- E. Chan, F. Van Zee, R. van de Geijn, E.S. Quintana-Ortí, G. Quintana-Ortí. Satisfying your dependencies with SuperMatrix. IEEE Cluster 2007.
- E. Chan, F.G. Van Zee, P. Bientinesi, E.S. Quintana-Ortí, G.
   Quintana-Ortí, R. van de Geijn. SuperMatrix: A multithreaded runtime scheduling system for algorithms-by-blocks. *Principles and Practices of Parallel Programming PPoPP'2008*.
- Brian Gunter, R. van de Geijn. Parallel OOC computation and updating of the QR factorization. ACM Trans. on Mathematical Software, 31(1):60-78, 2005.

# Related Approaches

QR Factorization algorithms on SMP & multi-core

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Algorithm-byblocks

Result

Remarks

#### Cilk (MIT) and CellSs (Barcelona SuperComputing Center)

- General-purpose parallel programming
  - Cilk → irregular problems
  - ullet CellSs o for the Cell B.E.
- High-level language based on OpenMP-like pramas + compiler + runtime system
- Moderate results for dense linear algebra

#### PLASMA (UTK – Jack Dongarra)

- Traditional style of implementing algorithms: Fortran-77
- Complicated coding
- Runtime system + ?

